Remarks

Claims 1-16 and 18-20 are pending in the application and are presented for reconsideration. Claims 1, 5, 10, 11, 14 and 19 have been amended; claim 17 has been canceled; and claims 2-4, 6-9, 12-13, 15-16, 18 and 20 remain in the application unchanged. No new matter has been added.

Claim Objections

Claims 10, 11 and 14 are objected to for various informalities.

a. In claim 10, the phrase "the application are" (line 9) has been replaced by the phrase —the applications are—.

b. In claim 11, the phrase "As embedded system" (line 1) has been replaced by the phrase --An embedded system--.

c. In claim 11, the phrase "in which application are" (line 2) has been replaced by the phrase --in which applications are--.

d. In claim 14, the phrase "wherein critical write include writes" has been replaced by the phrase --wherein critical writes include writes--.

The objections to the claims are believed to be overcome.

Claim Rejections

Claim 1 is rejected under 35 U.S.C. § 112, second paragraph as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

Claims 1-5, 10-13, 15, 18 and 19 are rejected under 35 U.S.C. § 102(b) as being anticipated by Olsen (US 2001/0032300).

Claim 6 is rejected under 35 U.S.C. § 103(a) as being unpatentable over Olsen in view of Aviani, Jr. (US 5,950,205).

Claims 8, 9, 14, 16 and 20 are rejected under 35 U.S.C. § 103(a) as being unpatentable over Olsen in view of Hill et al. (US2003/0221083).

Claim 20 is rejected under 35 U.S.C. § 103(a) as being unpatentable over Olsen in view of Raves et al. (US 2003/0182500).

Claims 1-20 stand rejected under 35 U.S.C. § 102(b) as being anticipated by Lui (U.S. Pat. No. 5,488,495).

Claims 1-4, 6-17, 19-20 stand rejected under 35 U.S.C. § 102(b) as being anticipated by Katsui (U.S. Pat. No. 5,953,208).

The Examiner's rejections of the claims are respectfully traversed in view of the amendments made herein.

Allowable Subject Matter

Claims 7 and 17 are objected to as being dependent upon a rejected base claim, but are said to be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

I. Rejection of Claims Under 35 U.S.C. § 112, Second Paragraph

Claim 1 is rejected under 35 U.S.C. § 112, second paragraph as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention. Specifically, the Examiner states that the limitation "the protected memory locations" in line lack antecedent basis in the claim. Claim 1 has been amended to provide antecedent basis for the objectionable limitation. The Applicant respectfully submits that the rejection of claim 1 under 35 U.S.C. § 112, second paragraph is now overcome.

II. Rejections of Claims Under 35 U.S.C. § 102

a. *Claims 1-9*

Applicant's Claim 1 recites:

A method of protecting memory locations associated with an embedded system, the method comprising:
starting a write filter that intercepts writes to protected memory locations and stores the writes in a cache;
starting a state machine with at least a change state and a normal state;
upon starting the state machine, entering the change state when an indication is present that data needs to be persisted to the protected memory locations, otherwise entering the normal state;

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in the normal state identifying requests for critical writes to the protected memory locations and creating at least one update file describing the critical writes, wherein the critical writes are not persisted to the protected memory locations during the normal state; and

in the change state, applying the critical writes described in the update file and rebooting the system in a manner that persists the critical writes to the protected memory locations.

Claim 1 has been amended to recite "in the normal state identifying requests for critical writes to the protected memory locations and creating at least one update file describing the critical writes, wherein the critical writes are not persisted to the protected memory locations during the normal state". None of Olsen, Aviani, Jr., Hill et al., or Raves et al. teaches or suggest the limitation "wherein the critical writes are not persisted to the protected memory locations during the normal state" as now recited in Applicant's Claim 1. Since from the statement of reasons for the indication of allowable subjects matter with regards to Claim 17, the Examiner agrees that none of the prior art of record teaches or suggests the application of updates during the normal state, the Applicant likewise respectfully submits that because Claim 1 now recites this limitation, Claim 1 is now allowable over the prior art of record. Accordingly, Applicant respectfully requests the Examiner to withdraw the rejection of Claim 1.

Claims 2-9 each depend from independent base Claim 1 and add further limitations. For at least the same reasons that Claim 1 is not shown, taught, or disclosed by the cited references, Claims 2-9 are likewise not shown, taught, or disclosed. Thus, Applicant respectfully submits that the rejection of Claims 2-9 should be withdrawn.

b. Claim 10

Claim 10 recites similar limitations to Claim 1, including "during operation in the normal state, the applications are run and when a critical write to the operating system is requested, the critical write is not persisted to the operating system but an update file is generated to store the critical write until the embedded system enters the change state ". For at least the same reasons that Claim 1 is not shown, taught, or disclosed by the cited references, Claim 10 is

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likewise not shown, taught, or disclosed. Thus, Applicant respectfully submits that the rejection of Claim 10 should be withdrawn.

c. Claims 11-20

Claim 11 recites similar limitations to Claim 1, including "a normal state in which applications are executed and a write filter intercepts writes to a protected memory location and redirects them to a non-protected memory location wherein the writes to the protected memory location are not applied to the protected memory location during the normal state ". For at least the same reasons that Claim 1 is not shown, taught, or disclosed by the cited references, Claim 11 is likewise not shown, taught, or disclosed. Thus, Applicant respectfully submits that the rejection of Claim 11 should be withdrawn.

Claims 12-20 each depend from independent base claim 11 and add further limitations. For at least the same reasons that Claim 11 is not shown, taught, or disclosed by the cited references, Claims 12-20 are likewise not shown, taught, or disclosed. Thus, Applicant respectfully submits that the rejection of claims 12-20 should be withdrawn.

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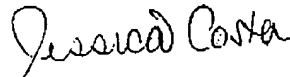Docket No. 10030673-1

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Conclusion

In view of the foregoing remarks, it is respectfully submitted that none of the references cited by the Examiner taken alone or in any combination shows, teaches, or discloses the claimed invention, and that Claims 1-16 and 18-20 are in condition for allowance. Reexamination and reconsideration are respectfully requested.

Should the Examiner have any questions regarding this amendment, or should the Examiner believe that it would further prosecution of this application, the Examiner is invited to call the undersigned.

Respectfully submitted,

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